

## Zhenyu Yan

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Birthday: 1988.01.20

Gender: Male

Location: Beijing

## Education

September.2010-September.2011 University of Limerick

### Master Engineering of VLSI system

QCA: 3.13

Semiconductor technology, Digital signal processing, Advantaged digital system design, Application specific integrated circuit, Testing engineering, Digital control, Noise

August.2006-August.2010 Xidian University

### Bachelor Engineering of Integrated Circuits Design and Integrated System

Result: 75/100

Digital circuits technology, Analogue circuits technology, Silicon technology, Technics of integrated circuit, Design of analogue CMOS integrated circuit, Design of digital integrated circuit, Verilog HDL, EDA technology etc.

## Project Experience

April.2016-Now

### Verification for a CPU SOC project cooperated with Intel in "Tsinghua"

1. Built the subsystem level UVM environment and support it.
2. Prepared SV testcases and made C testcases in environment using DPI.
3. Wrote scripts to deal with the requirement in simulation, log processing, regression etc.
4. Completed the verification work in module level such as Uart, SPI Flash, OTP, PVT, PCIECMDQ etc., subsystem level and participated in system level verification.
5. Supported verification work in Wuxi team with the RPU module.

June.2015-April.2016

### Verification for an ARMv8 based project in "Spreadtrum"

1. Participated in an ARMv8 based project in verification team.
2. Participated in Building the UVM environment and support it.
3. Wrote scripts to deal with the requirement in verification work.
4. Extracted the entries used in MMU from AVS.
5. Verified DPU module using testcases generated by GPRO and testcases wrote in ASM.
6. Verified the subsystem called pipe-control, which contained IFU and DPU.

December.2012-May.2015

### Design and Verification, DFT, testing for multiple projects in "Sigma Chip"

1. Participated in digital simulation in RTL level and chip level simulation using ASM language, Mixed signal simulation and wrote RTL codes for function description for analog modules in multiple projects.
2. Designed modules such as UART, PCA, DMA, Timer, SPI, WDT, I2C etc., and verified them.
3. Completed the DFT related work including ATPG and synthesizing scan chain.
4. Simulated some modules using C in KEIL environment, and test some of them in FPGA.
5. Researched to transfer technics library using NCX.

## Working Experience

April.2016-Present

Beijing, Full-Time

### **Tsinghua University | Digital Verification Engineer**

Participated in verification work, built the UVM environment and supported design team.

June.2015-April.2016

Beijing, Full-Time

### **Spreadtrum | Digital Verification Engineer**

Participated in verification work, built the UVM environment and supported design team.

October.2012-May.2015

Beijing, Full-Time

### **Beijing SigmaMicro Ltd. | Digital Design Engineer**

Participated in various projects for IC design, verification, testing and DFT etc.

## Technical Skills

<b>Program</b>	Skilled in use of Verilog HDL, SystemVerilog and UVM. Skilled in C Program
<b>Software</b>	Skilled in Cadence and Synopsys series tools.
<b>Script</b>	Skilled in Python, TCL, Bash shell etc.
<b>Language</b>	Chinese (native), English (fluent, IELTS: 6.5, CET-6)
<b>Operating System</b>	Familiar with Windows, Linux, Mac
<b>Other</b>	Full driving license

## Hobbies

<b>Website Building</b>	Familiar with popular CMS such as Joomla, Wordpress etc. Familiar with SEO. Built lots of website from server preparing to optimizing the application.
<b>Musical Instrument</b>	Familiar with Guitar, Drums, Piano.
<b>Music Production</b>	Independent Music Producer. Familiar with professional music producing tools such as Studio One, Logic Pro, Cubase, Sonar, T-racks etc. Wrote, arranged, mixed and released more than 60 songs and 8 music albums.
<b>Video Production</b>	Familiar with video processing tools such as Final Cut Pro etc. Produced 2 music video and some video projects.

## Self-statement

I am self-motivated and easily accept new things, and always positive to find method to solve problems. I think I am creative to deal with my study and work properly.

I am adaptive to change and fast learning. No matter project or laboratory, I did well. I prefer real work and application of knowledge.

I have some experience in collaborating with colleagues in achieving common goals. I also successfully organized some fund-raising events with my team.